



Master Time Table

A.Y. 2024-25 (Even)

DEPARTMENT OF ELECTRICAL ENGINEERING

Date(w.e.f.) 1-1-2025

DAY	CLASS	TIME							
		9.15 to 10.15	10.15 to 11.15	11.15 to 11.30	11.30 to 12.30	12.30 to 1.30	1.30 to 2.15	2.15 to 3.15	3.15 to 4.15
MON	SY	DCMT(PBK)	PE(AAP)	SHORT BREAK	PS I(RVV)	EV(X)	LUNCH BREAK	S1 - DCMT , S2 - PE , S3 - PS I , S4 - CS I	
	TY	EEAC(RVV)	DSP(VNA)		T1-EEAC,T2-EMD,T3PSSC,T4-ED			EMD(SSD)	LIBRARY
	B. Tech	B1- HVDC , B2- PP II			EHVAC (PBK)	EGUT(TMP)		HVDC(RBL)	LIBRARY
TUE	SY	CS I(SSD)	EM(USS)		DCMT(PBK)	PE(AAP)		S1 - CS I , S2 - DCMT , S3 - PE , S4 - PS I	
	TY	EEAC(RVV)	DSP(VNA)		T1-EITM,T2-EEAC,T3-EMD,T4-PSSC			PSSC(USS)	ED(RBL)
	B. Tech	B2-HVDC , B1- PP II			PLC & SCADA/EMEEA	MED(VNA)		EGUT(TMP)	SEMINAR(T)
WED	SY	PS I(RVV)	CS I(SSD)		PE(AAP)	EV(X)		S1 - PS I , S2 - CS I , S3 - DCMT , S4 - PE	
	TY	DSP(VNA)	PSSC(USS)		T1-ED,T2-EITM,T3-EEAC,T4-EMD			EMD(SSD)	ED(RBL)
	B. Tech	B1,B2- PP II			PLC & SCADA/EMEEA	HVDC(RBL)		MED (VNA)	SEMINAR(T)
THR	SY	EM(USS)	DCMT(PBK)		EV(X)	PS I(RVV)		S1- PE , S2 - PS I , S3- CS I , S4 - DCMT	
	TY	EMD(SSD)	DSP(VNA)		T1-EMD,T2-PSSC,T3-ED,T4-EITM			c ++ (MV)	PSSC(USS)
	B. Tech	B1,B2- PP II			MED (VNA)	EHVAC (PBK)		HVDC(RBL)	SEMINAR(T)
FRI	SY	EM(USS)	PS I(RVV)	CS I(SSD)	DCMT(PBK)	T & P/Internship			
	TY	EMD(SSD)	ED(RBL)	T1PSSC,T2-ED,T3-EITM,T4-EEAC		PSSC(USS)	EITM(T)(AAS)		
	B. Tech	Course Enrichment/T & P/Internship		EHVAC (PBK)	EGUT(TMP)	PLC & SCADA/EMEEA	Interaction		
SAT	SY	EM (T) (USS)	Internship	Internship	c ++ (MV)	c ++ (MV)	LIBRARY		
	TY	EITM(T)(AAS)	DSP(T)(VNA)	EMD(SSD)(T)	T & P/Internship	Course Enrichment			
	B. Tech	EHVAC (T)(PBK)	EGUT(T)(TMP)	Project Review		Project Review			

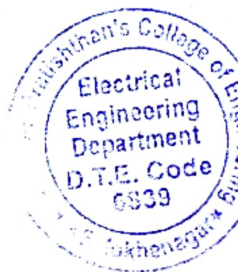
*Hethkar*  
Time Table  
coordinator

*[Signature]*  
HOD

*[Signature]*  
Dean (Academics)

*[Signature]*  
IQAC (Head)

*[Signature]*  
Principal





Doc. No: DYP-ACAD-AP-04a  
Page 1 of 1

Dr. D. Y. Patil Pratishthan's College of Engineering, Salokhenagar, Kollhapur

Revision No. 01

Revision Date: 01/12/2023

Second Year Time Table (Classroom No. 217 )

A.Y. 2024-25 (Even)

DEPARTMENT OF ELECTRICAL ENGINEERING

Date(w.e.f.) - 1-1-2025

DAY	CLASS	TIME						
		9.15 to 10.15	10.15 to 11.15	11.15 to 11.30	11.30 to 12.30	12.30 to 1.30	1.30 to 2.15	2.15 to 3.15
MON	SY	DCMT(PBK)	PE(AAP)	SHORT BREAK	PS (RVV)	EVS(X)	LUNCH BREAK	S1 - DCMT, S2 - PE, S3 - PS1, S4 - CS1
TUE	SY	CS I(SSD)	EM(USS)		DCMT(PBK)	PE(AAP)		PE(AAP)
WED	SY	PS I(RVV)	CS I(SSD)	SHORT BREAK	PE(AAP)	EVS(X)	LUNCH BREAK	S1 - PS1, S2 - CS1, S3 - DCMT, S4 - PE
THR	SY	EM(USS)	DCMT(PBK)		EVS(X)	PS I(RVV)		PS I(RVV)
FRI	SY	EM(USS)	PS I(RVV)	SHORT BREAK	CS I(SSD)	DCMT(PBK)	LUNCH BREAK	T & P/Internship
SAT	SY	EM (T) (USS)	Internship		Internship	c++ (MV)		c++ (MV)

*Salokhenagar*  
Time Table  
coordinator

*[Signature]*  
HOD

*[Signature]*  
Dean (Academics)

*[Signature]*  
IQAC (Lead)

*[Signature]*  
Principal





**Dr. D. Y. Patil Pratishthan's College of Engineering , Salokhenagar, Kolhapur**

Doc. No: DYP-ACAD-AP-04a

Revision No. 01

Page 1 of 1

Revision Date: 01/12/2023

**Third Year Time Table (Classroom No. 208)**

**A.Y. 2024-25 (Even)**

**DEPARTMENT OF ELECTRICAL ENGINEERING**

Date(w.e.f.) - 1-1-2025

DAY	CLASS	TIME							
		9.15 to 10.15	10.15 to 11.15	11.15 to 11.30	11.30 to 12.30	12.30 o 1.30	1.30 to 2.15	2.15 to 3.15	3.15 to 4.15
MON	TY	EEAC(RVV)	DSP(VNA)	SHORT BREAK	T1-EEAC,T2-EMD,T3PSSC,T4-ED (PSSC - USS + AAS)		LUNCH BREAK	EMD(SSD)	LIBRARY
TUE	TY	EEAC(RVV)	DSP(VNA)		T1-EITM,T2-EEAC,T3-EMD,T4-PSSC (PSSC - USS + TMP)			PSSC(USS)	ED(RBL)
WED	TY	DSP(VNA)	PSSC(USS)		T1-ED,T2 -EITM,T3-EEAC,T4-EMD (PSSC - USS + PBK)			EMD(SSD)	ED(RBL)
THR	TY	EMD(SSD)	DSP(VNA)		T1-EMD,T2-PSSC,T3-ED,T4-EITM (PSSC - USS + ABC)			c ++ (MV)	PSSC(USS)
FRI	TY	EMD(SSD)	ED(RBL)		T1PSSC,T2-ED,T3-EITM,T4-EEAC (PSSC - USS + MV)			PSSC(USS)	EITM(T)(AAS)
SAT	TY	EITM(T)(AAS)	DSP(T)(VNA)		EMD(SSD)(T)	T & P/Internship		Course Enrichment	

*Halkar*  
Time Table  
coordinator

*[Signature]*  
HOD

*[Signature]*  
Dean (Academics)

*[Signature]*  
IQAC (Head)

*[Signature]*  
Principal



**B . Tech Time Table**


**A.Y. 2024-25 (Even)**

**DEPARTMENT OF ELECTRICAL ENGINEERING**

Date(w.e.f.) - 1-1-2025

DAY	CLASS	TIME							
		9.15 to 10.15	10.15 to 11.15	11.15 to 11.30	11.30 to 12.30	12.30 to 1.30	1.30 to 2.15	2.15 to 3.15	3.15 to 4.15
MON	B. Tech	B1- HVDC , B2- PP II		SHORT BREAK	EHVAC (PBK) (208)	EGUT(TMP) (208)	LUNCH BREAK	HVDC(RBL) (217)	LIBRARY
TUE	B. Tech	B2-HVDC , B1- PP II			PLC & SCADA/EMEEA (208)	MED (VNA) (208)		EGUT(TMP) (217)	SEMINAR(T)
WED	B. Tech	B1,B2- PP II			PLC & SCADA/EMEEA (208)	HVDC(RBL) (208)		MED (VNA) (217)	SEMINAR(T)
THR	B. Tech	B1,B2- PP II			MED (VNA) (208)	EHVAC (PBK) (208)		HVDC(RBL) (217)	SEMINAR(T)
FRI	B. Tech	Course Enrichment/T & P/Internship			EHVAC (PBK) (208)	EGUT(TMP) (208)		PLC & SCADA/EMEEA (217)	Interaction
SAT	B. Tech	EHVAC (T)(PBK) ( )	EGUT(T)(TMP) (217)		Project Review			Project Review	

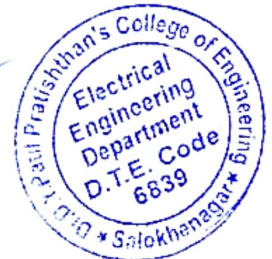
  
Time Table  
coordinator

  
HOD

  
Dean (Academics)

  
IQAC (Head)

  
Principal





Dr. D. Y. Patil Pratishthan's College of Engineering , Salokhenagar, Kolhapur 416007

Doc. No: DYP-ACAD-AP-04a

Revision No. 01

Page 1 of 1

Revision Date: 01/12/2023

**Classroom Occupancy (Classroom No. 217 )**

**DEPARTMENT OF ELECTRICAL ENGINEERING**

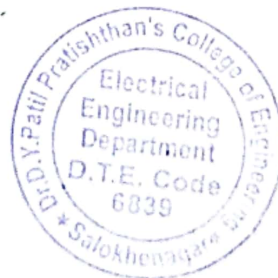
**A.Y. 2024-25 (Even)**

Date(w.e.f.) - 1-1-2025

**Class- SY/B.Tech**

DAY	TIME							
	9.15 to 10.15	10.15 to 11.15	11.15 to 11.30	11.30 to 12.30	12.30 to 1.30	1.30 to 2.15	2.15 to 3.15	3.15 to 4.15
MON	DCMT(PBK) (SY)	PE(AAP) (SY)	SHORT BREAK	PS I(RVV) (SY)	EVS(X) (SY)	LUNCH BREAK	HVDC(RBL) (B. Tech)	
TUE	CS I(SSD) (SY)	EM(USS) (SY)		DCMT(PBK) (SY)	PE(AAP) (SY)		EGUT(TMP) (B. Tech)	SEMINAR(T) (B. Tech)
WED	PS I(RVV) (SY)	CS I(SSD) (SY)		PE(AAP) (SY)	EVS(X) (SY)		MED (VNA) (B. Tech)	SEMINAR(T) (B. Tech)
THR	EM(USS) (SY)	DCMT(PBK) (SY)		EVS(X) (SY)	PS I(RVV) (SY)		HVDC(RBL) (B. Tech)	SEMINAR(T) (B. Tech)
FRI	EM(USS) (SY)	PS I(RVV) (SY)		CS I(SSD) (SY)	DCMT(PBK) (SY)		PLC & SCADA/EMEEA (B. Tech)	T & P/Internship (SY)
SAT	EM (T) (USS) (SY)	EGUT(T)(TMP) (B. Tech)		Internship (SY)	c ++ (MV) (SY)		c ++ (MV) (SY)	

Prepared by: Time Table coordinator



Verified by: HOD



**Classroom Occupancy (Classroom No. 208)**

**A.Y. 2024-25 (Even)**

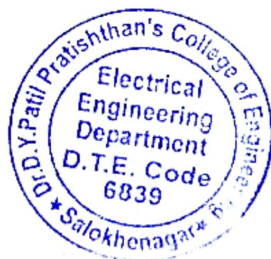
**DEPARTMENT OF ELECTRICAL ENGINEERING**

Date(w.e.f.) - 1-1-2025

Class- TY/B.Tech

DAY	TIME							
	9.15 to 10.15	10.15 to 11.15	11.15 to 11.30	11.30 to 12.30	12.30 to 1.30	1.30 to 2.15	2.15 to 3.15	3.15 to 4.15
MON	EEAC(RVV) (TY)	DSP(VNA) (TY)	<b>SHORT BREAK</b>	EHVAC (PBK) (B. Tech)	EGUT(TMP) (B. Tech)	<b>LUNCH BREAK</b>	EMD(SSD) (TY)	
TUE	EEAC(RVV) (TY)	DSP(VNA) (TY)		PLC & SCADA/EMEEA (B. Tech)	MED (VNA) (B. Tech)		PSSC(USS) (TY)	ED(RBL) (TY)
WED	DSP(VNA) (TY)	PSSC(USS) (TY)		PLC & SCADA/EMEEA (B. Tech)	HVDC(RBL) (B. Tech)		EMD(SSD) (TY)	ED(RBL) (TY)
THR	EMD(SSD) (TY)	DSP(VNA) (TY)		MED (VNA) (B. Tech)	EHVAC (PBK) (B. Tech)		c ++ (MV) (TY)	PSSC(USS) (TY)
FRI	EMD(SSD) (TY)	ED(RBL) (TY)		EHVAC (PBK) (B. Tech)	EGUT(TMP) (B. Tech)		PSSC(USS) (TY)	EITM(T)(AAS) (TY)
SAT	EITM(T)(AAS); (TY)	DSP(T)(VNA) (TY)		EMD(SSD)(T) (TY)	T & P/Internship (TY)		Course Enrichment (TY)	

*Jhankar*  
Prepared by: Time Table coordinator



*[Signature]*  
Verified by: HOD



Dr. D. Y. Patil Pratishthan's College of Engineering  
Salokhenagar, Kolhapur

LAB TIME TABLE

Department of Electrical Engineering	Year: 2024-25	Doc. No: DYP-ACAD-FRM-04a
Class :FE SE TE BE	Semester :EVEN	Revision No. 00
		Revision Date:01/07/2019

Name of Laboratory: Electrical Machine/Electrical Machine Design

Day	TIME	
	09:15 to 11:15	11:30 to 1:30
MON		T2 - EMD
		SSD
		T3 - EMD
		SSD
TUE		T4 - EMD
		SSD
		T1 - EMD
		SSD
WED		
THU		
FRI		
SAT		

Total Load: 16HR

Lab Utilization: 44%

SHORT FORM	SUBJECT	SHORT FORM	NAME
DCMT	DC Machine and transformer	PBK	MS. P. B. Kadam
EMD	Electrical Machine Design	SSD	Mr. S. S. Deshpande

Prepared By : Time Table Coordinator  
(Name & Sign)

*Dehkar*

Verified By: HOD





Dr. D. Y. Patil Pratishthan's College of Engineering  
Salokhenagar, Kolhapur

**LAB TIME TABLE**

Department of Electrical Engineering

Year: 2024-25

Doc. No: DYP-ACAD-FRM-04a

Revision No. 00

Class :FE|SE||TE||BE

Semester :EVEN

Revision Date:01/07/2019

Name of Laboratory: Power Electronics / EITM

Day	TIME		
	09:15 to 11:15		
MON		11:30 to 1:30	2:15 to 4:15
		T1 – EEAC	S2 – PE
		ASS	AAP
TUE		T2 – EEAC	S3 – PE
		RVV	AAP
WED		T3 – EEAC	S4 – PE
		RVV	AAP
THU		T4 – EEAC	S1 – PE
		RVV	AAP
FRI			
SAT			

LONG RECESS

SHORT RECESS

Total Load: 16 Hr

Lab Utilization: 44%

SHORT FORM	SUBJECT	SHORT FORM	NAME
PE	Power Electronics	AAP	Mr. A. A. Prabhavalikar
EEAC	Electrical Energy Audit and Conservation	RVV	Ms. R. V. Vader



*Prabhavalikar*  
Prepared By : Time Table Coordinator  
(Name & Sign)

*[Signature]*  
Verified By: HOD



Dr. D. Y. Patil Pratishthan's College of Engineering  
Salokhenagar, Kolhapur

LAB TIME TABLE

Doc. No: DYP-ACAD-FRM-04a

Department of Electrical Engineering

Year: 2024-25

Revision No. 00

Class :FE|SE||TE||BE

Semester :EVEN

Revision Date:01/07/2019

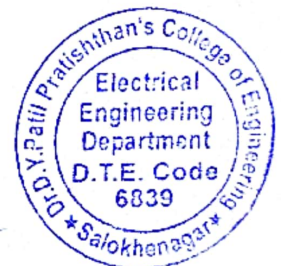
Name of Laboratory: Electrical Drive/PS I

Day	TIME				
	09:15 to 11:15		11:30 to 1:30		
MON		LONG RECESS	ED- T4	SHORT RECESS	PS I – S3
			RBL		RVV
TUE					PS I – S4
					RVV
WED			ED- T1		PS I – S1
			VNA		RVV
THU			ED- T3		PS I – S2
			RBL		RVV
FRI		ED- T2			
		RBL			
SAT					

Total Load: 20Hr

Lab Utilization: 56%

SHORT FORM	SUBJECT	SHORT FORM	NAME
ED	Electrical Drive	RBL/VNA	Prof. R. B. Lohar/ Dr. Vidya N. A.
PS I	Power System I	RVV	Ms. R. V. Vader



*Hlohnkar*  
Prepared By : Time Table Coordinator  
(Name & Sign)

*[Signature]*  
Verified By: HOD



Dr. D. Y. Patil Pratishthan's College of Engineering  
Salokhenagar, Kolhapur

**LAB TIME TABLE**

Doc. No: DYP-ACAD-FRM-04a

Department of Electrical Engineering

Year: 2024-25

Revision No. 00

Class :FE|SE||TE||BE

Semester :EVEN

Revision Date:01/07/2019

Name of Laboratory: EITM

Day	TIME					
	09:15 to 11:15	LONG RECESS	11:30 to 1:30	SHORT RECESS	2:15 to 4:15	
MON						
TUE			T1 – EITM			
			ASS			
WED			T2 – EITM			
			ASS			
THU			T4 – EITM			
			ASS			
FRI		T3 – EITM				
		ASS				
SAT						

Total Load: 8 Hr

Lab Utilization: 22%

SHORT FORM	SUBJECT	SHORT FORM	NAME
EITM	Electrical Installations Testing And Maintenance	ASS	Mr. A. S. Shinde



Prepared By : Hehtkar  
Time Table Coordinator  
(Name & Sign)

Verified By: [Signature]  
HOD



Dr. D. Y. Patil Pratishthan's College of Engineering  
Salokhenagar, Kolhapur

**LAB TIME TABLE**

Doc. No: DYP-ACAD-FRM-04a

Department of Electrical Engineering

Year: 2024-25

Revision No. 00

Class :FE|SE||TE||BE

Semester :EVEN

Revision Date:01/07/2019

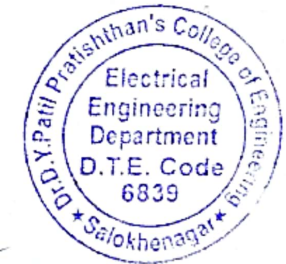
Name of Laboratory: Computer Lab

Day	TIME				
	09:15 to 11:15		11:30 to 1:30		
MON	HVDC- B1	LONG RECESS	T3 – PSSC	SHORT RECESS	2:15 to 4:15
	RBL		USS		S4 - CS I
TUE	HVDC- B2		T4 – PSSC	S1- CS I	
	RBL		USS	ASS	
WED				S2- CS I	
				ASS	
THU			T2 – PSSC	S3- CS I	
			USS	ASS	
FRI			T1 – PSSC		
			USS		
SAT					

Lab Utilization: 56%

Total Load: 20 Hr

SHORT FORM	SUBJECT	SHORT FORM	NAME
CS - I	Control System I	ASS	Mr. A. A. Shinde
PSSC	Power System Stability and Control	USS	Dr. U . S. Satpute
HVDC	HVDC	RBL	Prof. R. B. Lohar



*[Signature]*  
Verified By: HOD

*[Handwritten signature]*