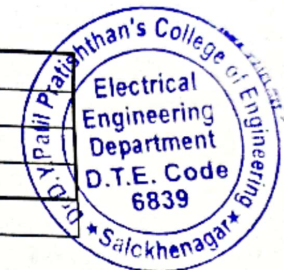




Master Time Table

DEPARTMENT OF ELECTRICAL ENGINEERING

A.Y. 2025-26 (Odd)



Date(w.e.f.) 8/09/2025

DAY	CLASS	TIME								
		9.15 to 10.15	10.15 to 11.15	11.15 to 11.30	11.30 to 12.30	12.30 o 1.30	1.30 to 2.15	2.15 to 3.15	3.15 to 4.15	
MON	SY	EM(AAS)	ECA(PD)	SHORT BREAK	S1- EM, S2- ECA, S3- OE I		LUNCH BREAK	S&S (AAP)(T)	UHV (VNA)	
	TY	ACM(ABC)	S&S (AAP)		DEM(AAS)	ACS(SSD)		T1- DEM, T2-MATLAB, T3-ACM		
	B. Tech	B1- CMPS, B2- SAP, B3-PP I			EV (VNA)	CMPS(USS)		PQH(RBL)	FACTS (TMP)	
TUE	SY	SSD (SDM)	MDM I(VNA)		S1-OE I, S2- EM, S3- ECA			ECA(PD)	S&S (AAP)	
	TY	S&S (AAP)	MATLAB (PBK)		ACM(ABC)	DEM(AAS)		T1-ACM, T2- DEM, T3- MATLAB		
	B. Tech	B1- PP I, B2- CMPS, B3-SAP			SAP(SSD)	EV (VNA)		FACTS (TMP)	PQH(RBL)	
WED	SY	SSD (SDM)	MDM I(VNA)		S1- ECA, S2- OE I, S3- EM			S&S (AAP)	LIBRARY	
	TY	S&S (AAP)	ACM(ABC)		EAL (PBK)	PS II(USS)		T1- MATLAB, T2-ACM, T3- DEM		
	B. Tech	B1- SAP, B2- PP I, B3- CMPS			ITP	SAP(SSD)		PQH(RBL)	FACTS (TMP)	
THR	SY	UHV (VNA)	S&S (AAP)		Hobby Project			ECA(PD)	EM(AAS)	
	TY	DEM(AAS)	EAL (PBK)		PS II(USS)	ACS(SSD)		T1- PS II, T2- ACS, T3- EAL		
	B. Tech	B1, B2 & B3 - PP I			SAP(SSD)	EV (VNA)		CMPS(USS)	ITP	
FRI	SY	EM(AAS)	OE I (TMP)		OE I (TMP)	OE I (Faculty)		ECA(PD)*	S&S (AAP)*	
	TY	ACS(SSD)	PS II(USS)		T1- ACS, T2- EAL, T3- PS II			T1- EAL, T2- PS II, T3- ACS		
	B. Tech	B1, B2 & B3 - PP I			CMPS(USS)	FACTS (TMP)*		EV (VNA)*	CMPS(USS)*	
SAT	SY	Value Added Course			EM(AAS)*	ECA(PD)*		EM(AAS)*	T & P	
	TY	S&S (AAP)(T)	LIBRARY		Value Added Course			T & P	NPTEL	
	B. Tech	EV (VNA)(T)	PQH(RBL)(T)		Value Added Course			LIBRARY	Internship	

Time Table Coordinator
Time Table Coordinator

Head of Department
Head of Department
Electrical Engineering
Dr. D. Y. Patil Pratishthan's
College of Engineering

Dean (Academics)
Dean (Academics)

IOAC (Head)
IOAC (Head)

Principal
Principal